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RMA  
(00068762)  
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裴亮樂  
王和樂

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AMD - PLATFORM HARDWARE ENG  
No.669, HUANKE ROAD  
SHANGHAI, CHINA 201203



SHEET: TOC

DATE: Fri Jun 21 05:25:22 2019

SHEET NUMBER: 1 OF 35

DOCUMENT NUMBER: 105\_D190w\_008

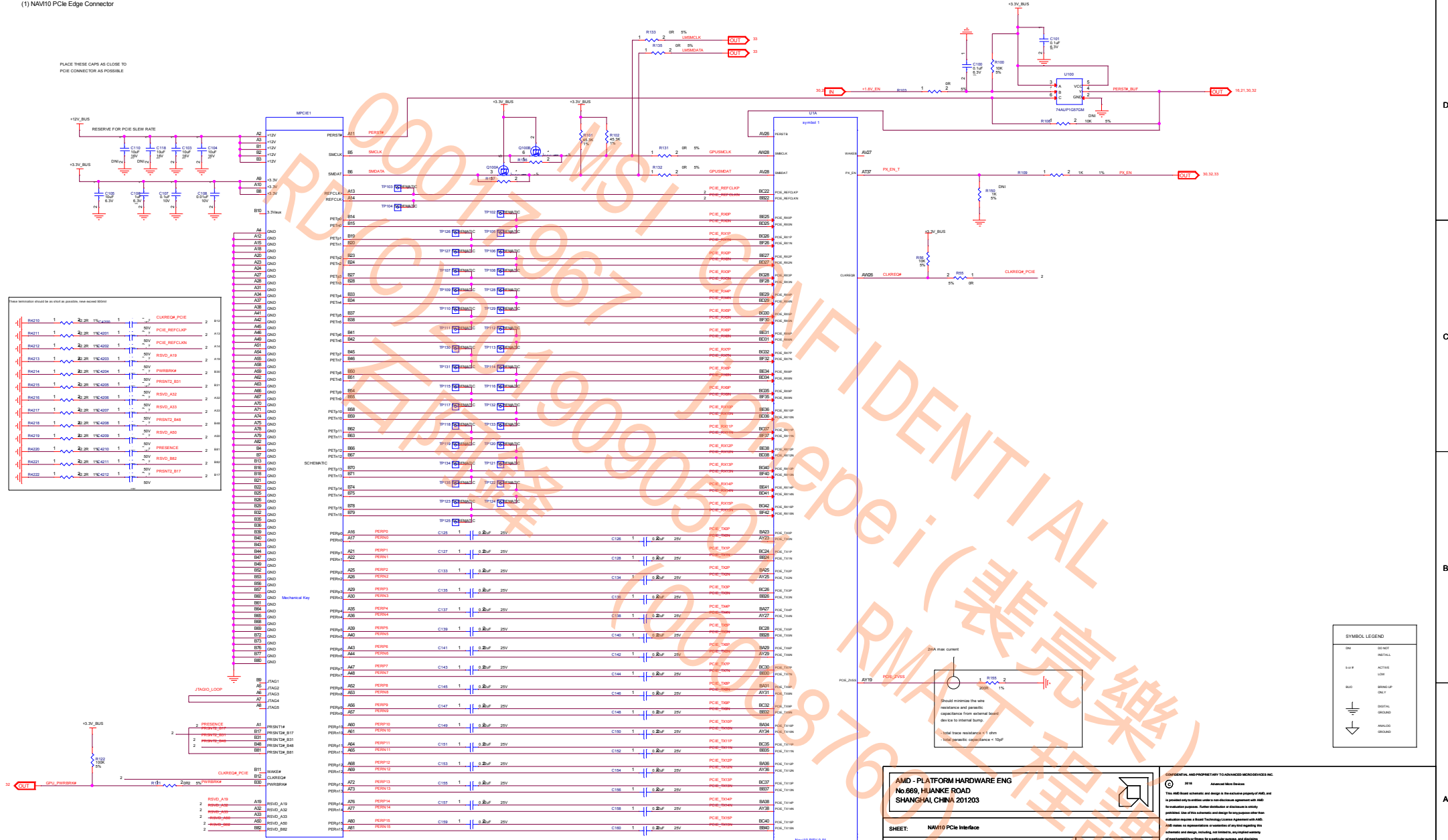
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TITLE: NAV110 G8X16mode DP DP HDMI DP

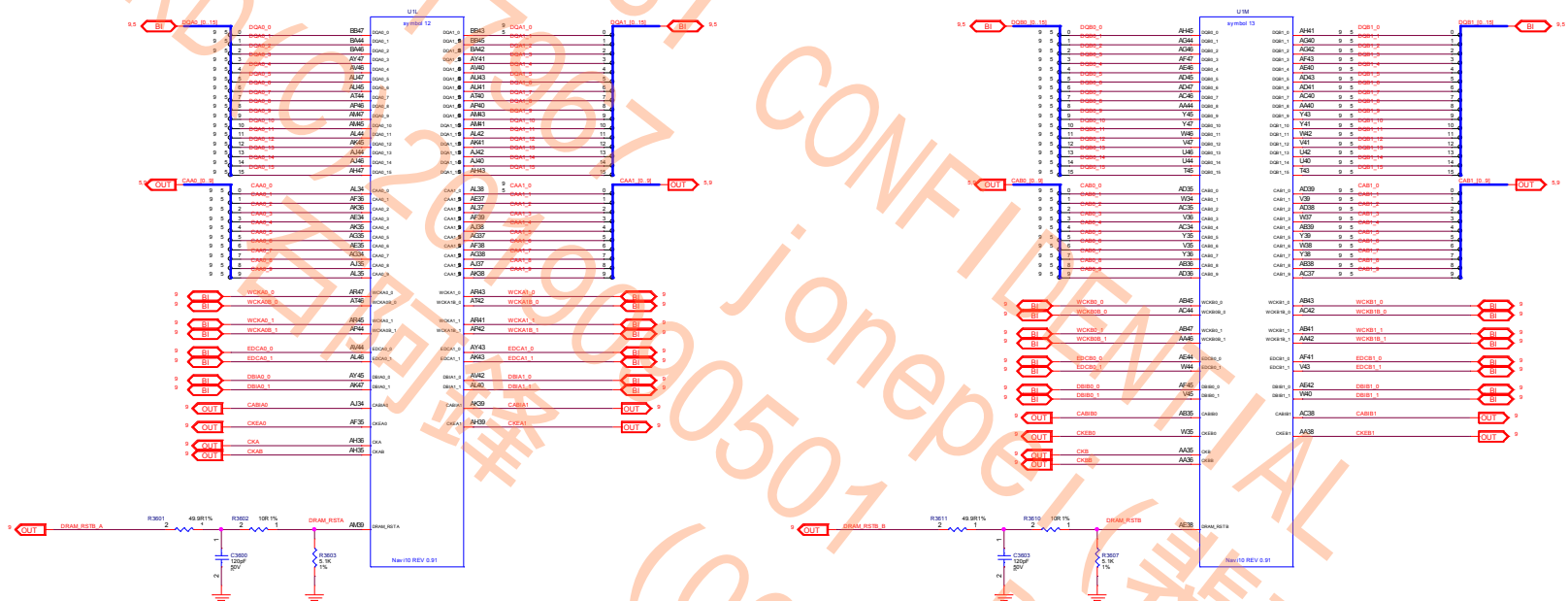
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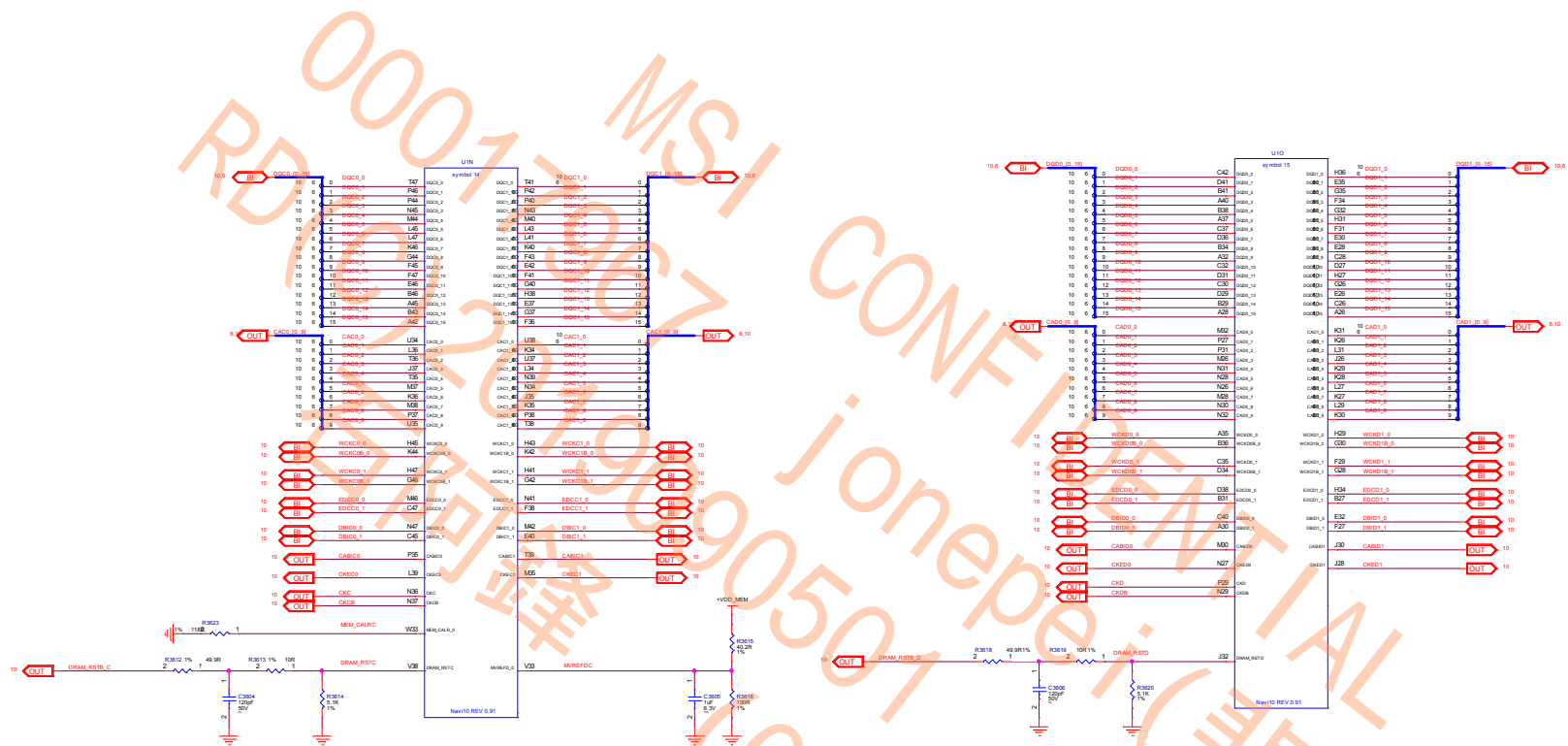
PLACE THESE CAPS AS CLOSE TO











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No.669, HUANKE ROAD  
SHANGHAI, CHINA 201203

SHEET: NAVI10 MEMCHCD

DATE: Fri Jun 21 05:25:24 2019

REV: 1.00

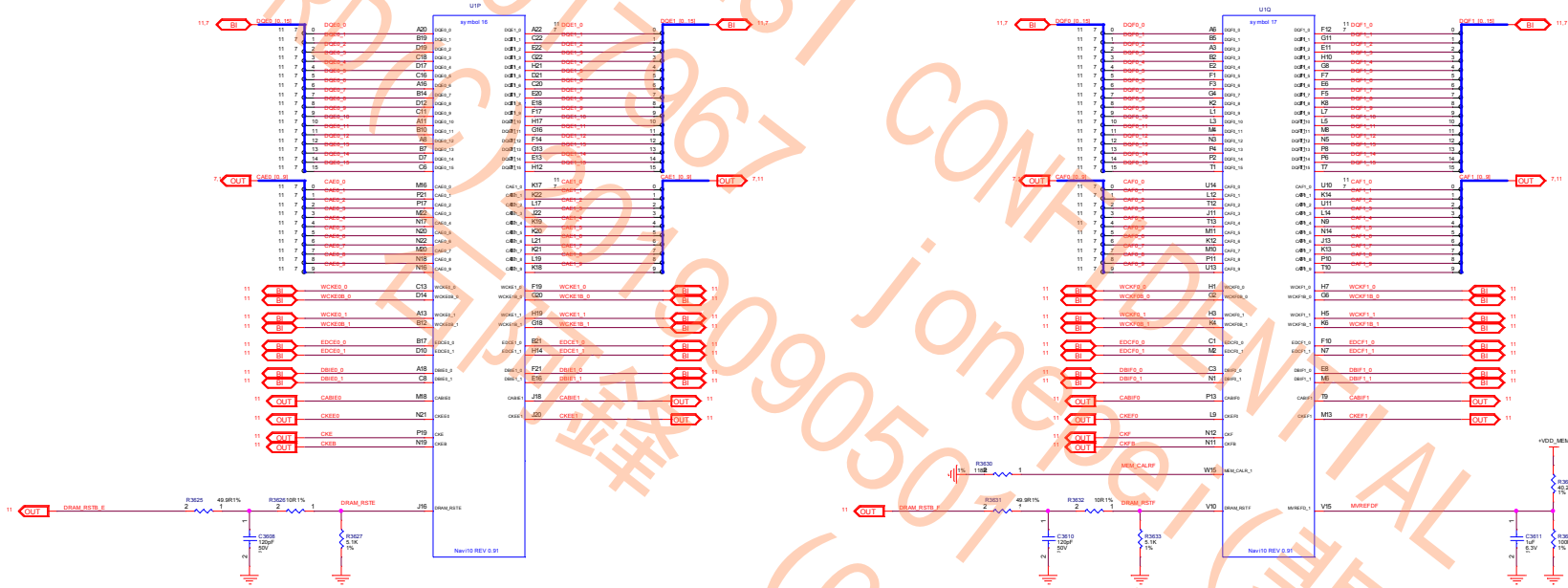
SHEET NUMBER: 6 OF 35


DOCUMENT NUMBER: 105\_0199w\_008

NOTES: NOTE

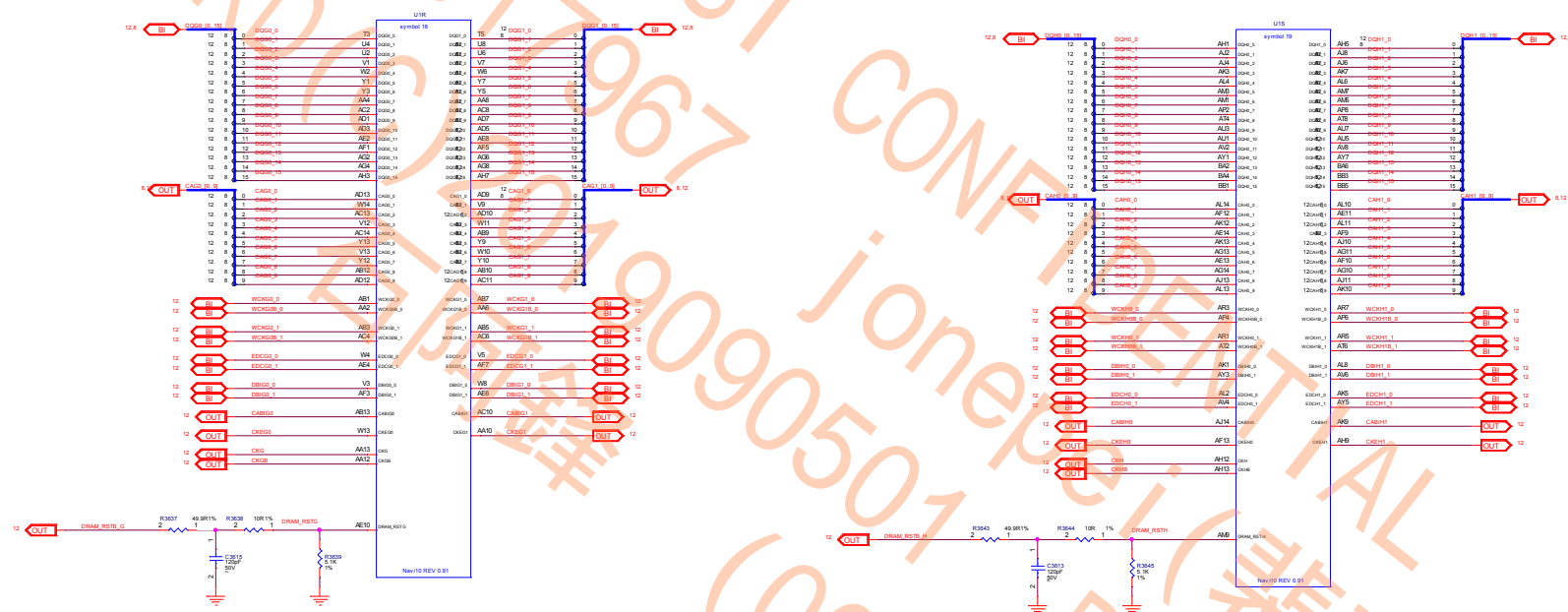
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TITLE: NAVI10 G8X16mode DP DP HDMI DP



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SHEET:    NAVI10 MEM CHEF		REV:    1.00		TITLE:    NAVI10 G8X16mode DP DP HDMI DP	
DATE:    Fri Jun 21 05:25:25 2019					
SHEET NUMBER:       7       OF    35					
DOCUMENT NUMBER:       105_0190w_008					
NOTES:    NOTE					





AMD - PLATFORM HARDWARE ENG  
No.669, HUANKE ROAD  
SHANGHAI, CHINA 201203

SHEET: NAVI10 MEMCHG

DATE: Fri Jun 21 05:25:26 2019

SHEET NUMBER: 8 OF 35

DOCUMENT NUMBER: 105\_0190w\_008

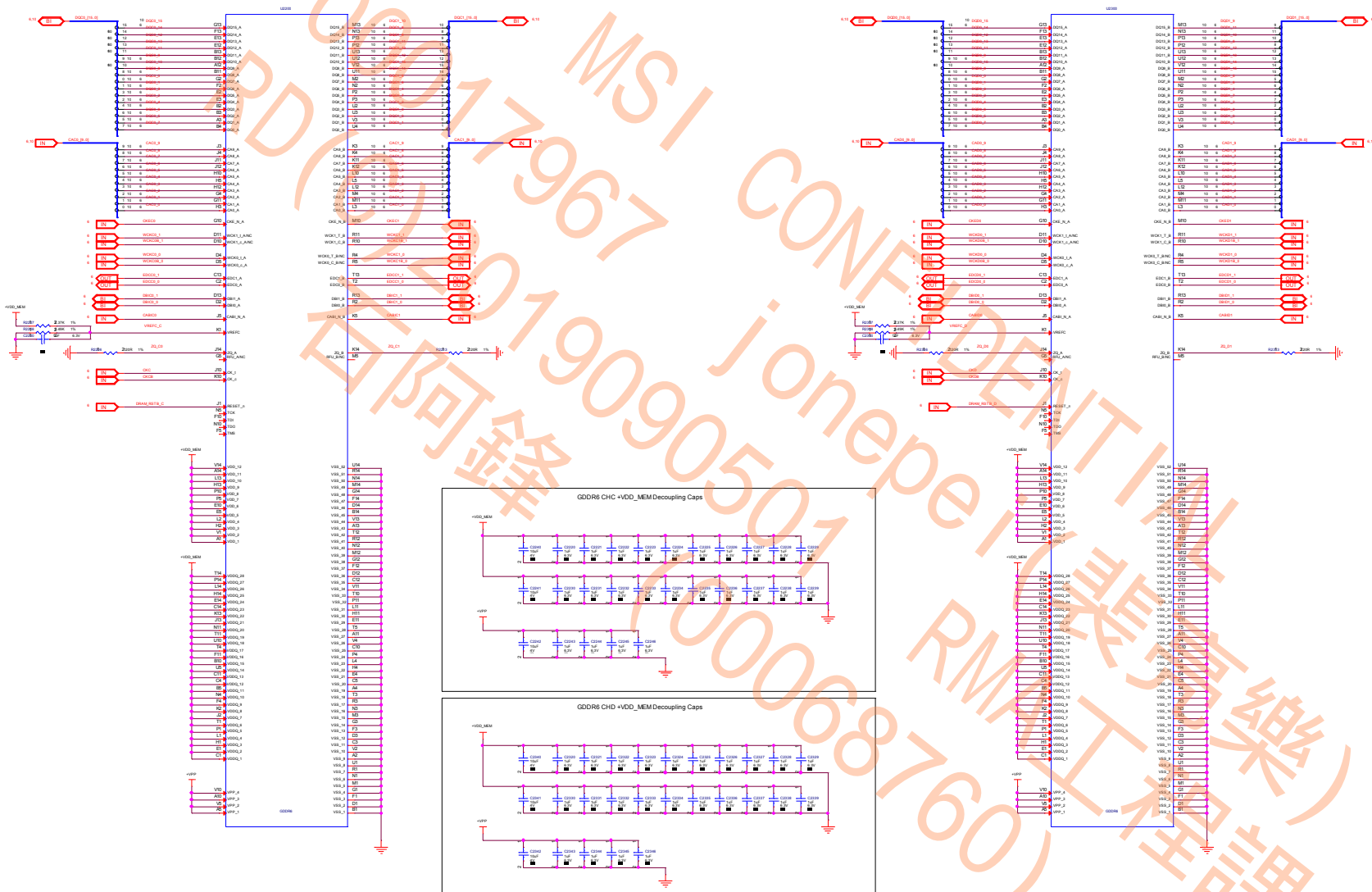
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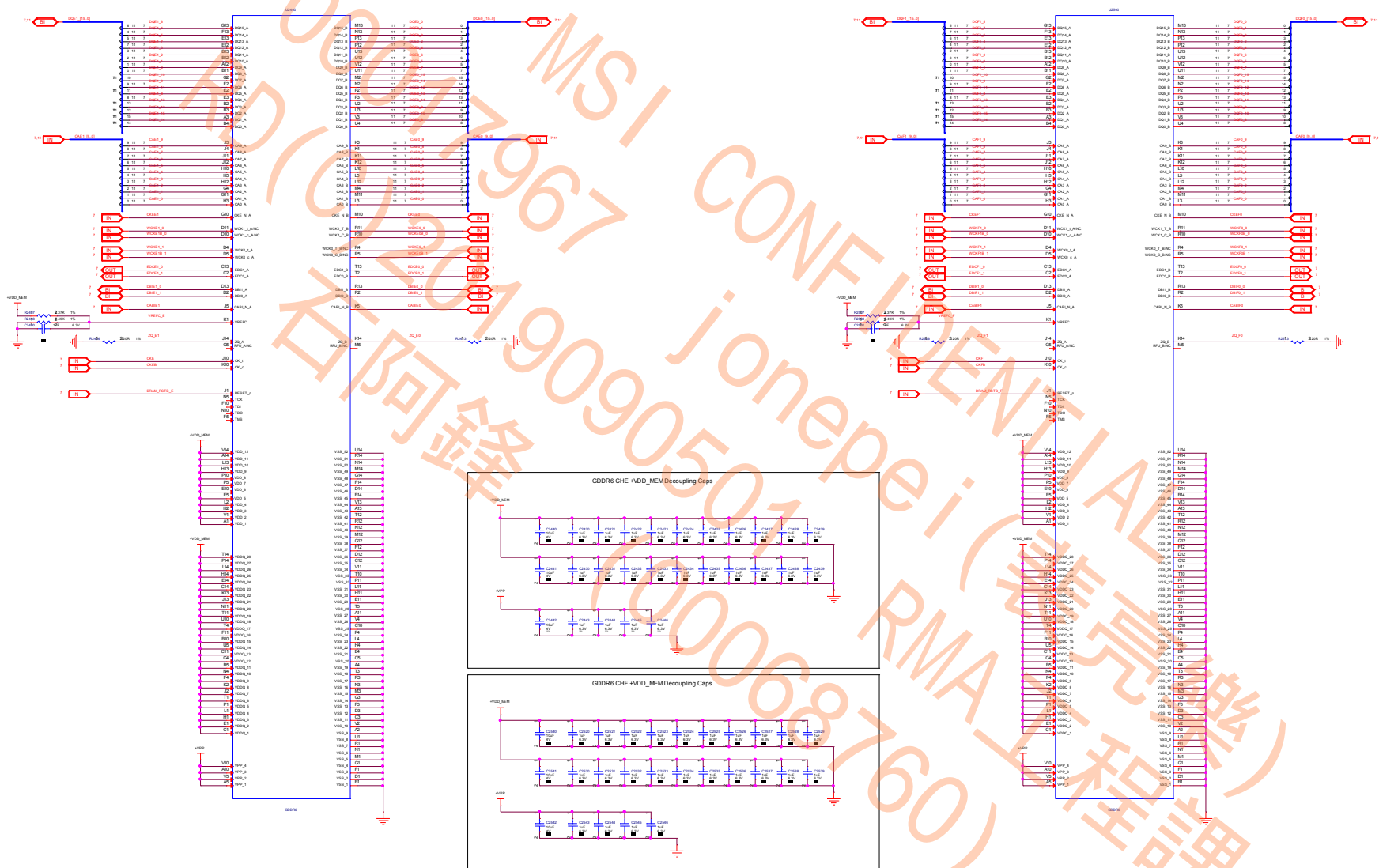
TITLE: NAVI10 G8X16mode DP DP HDMI DP



# GDDR6x16 CHCD Memory



<b>AMD - PLATFORM HARDWARE ENG</b> No.009, HUANKE ROAD SHANGHAI, CHINA 201203		SHEET: GDDR6 MEM CHCD DATE: Fri Jun 21 03:45:56 2019 DOCUMENT NUMBER: 101_0190x_008 NOTES: NOTE	REV: 1.00 TITLE: NAV10 G6X16mode DP DP HDMI DP
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AMD - PLATFORM HARDWARE ENG  
No. 888, HUANKE ROAD  
SHANGHAI, CHINA 201203

SHEET: GDDR6 MEM CHEF

DATE: F4 Jan 21 03:45:57 2019

REV: 1.00

SHEET NUMBER: 11 OF 35

DOCUMENT NUMBER: 105\_0190x\_088

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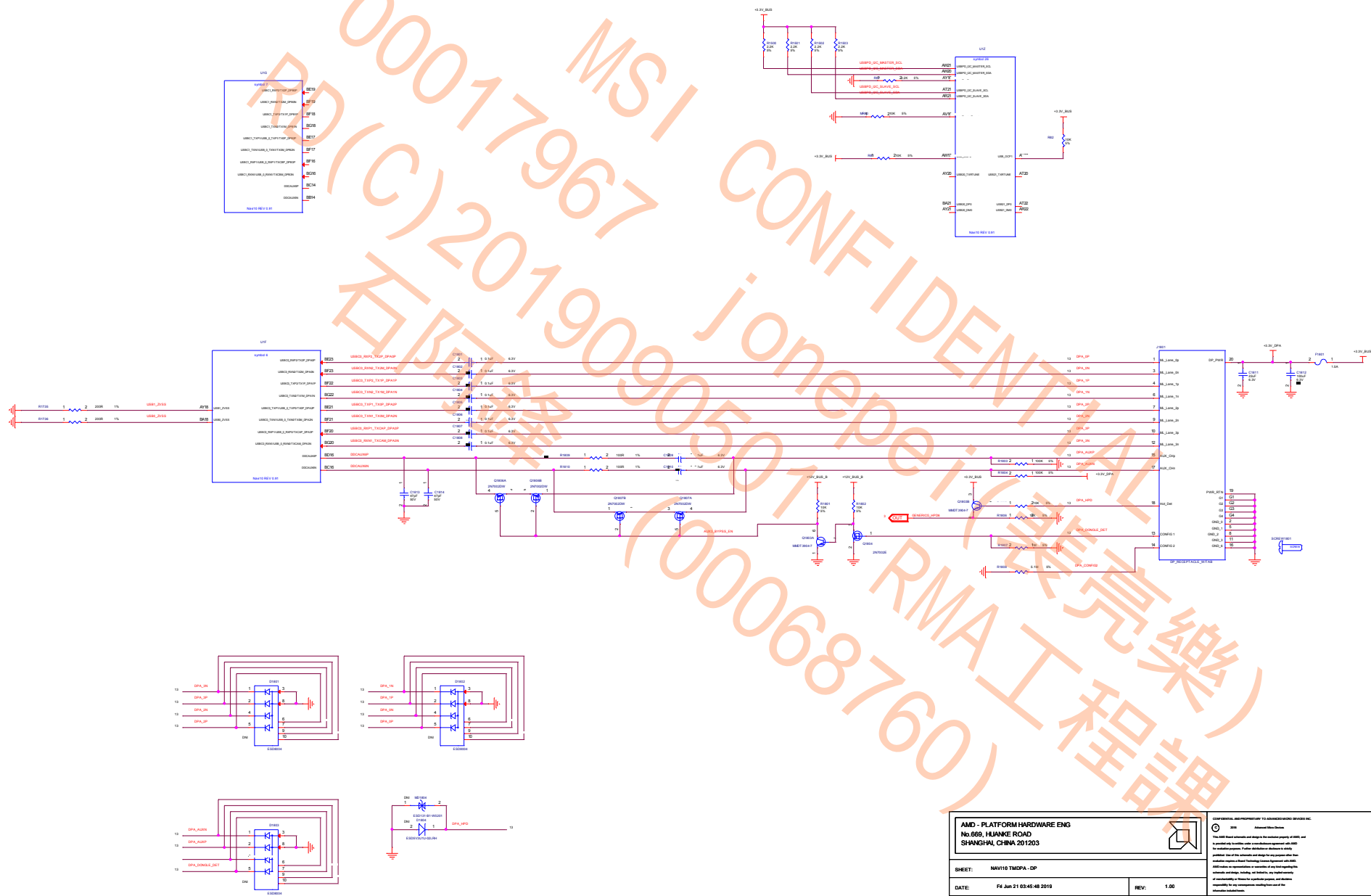
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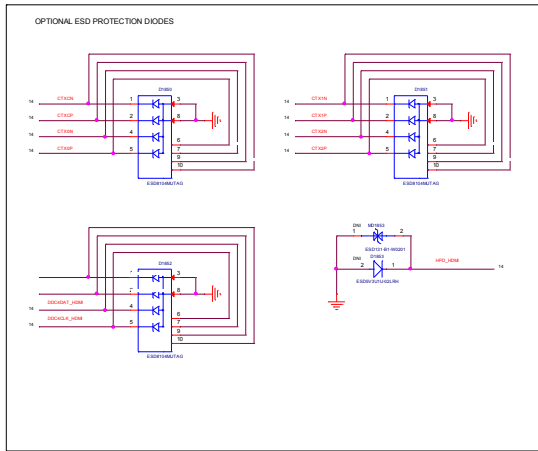
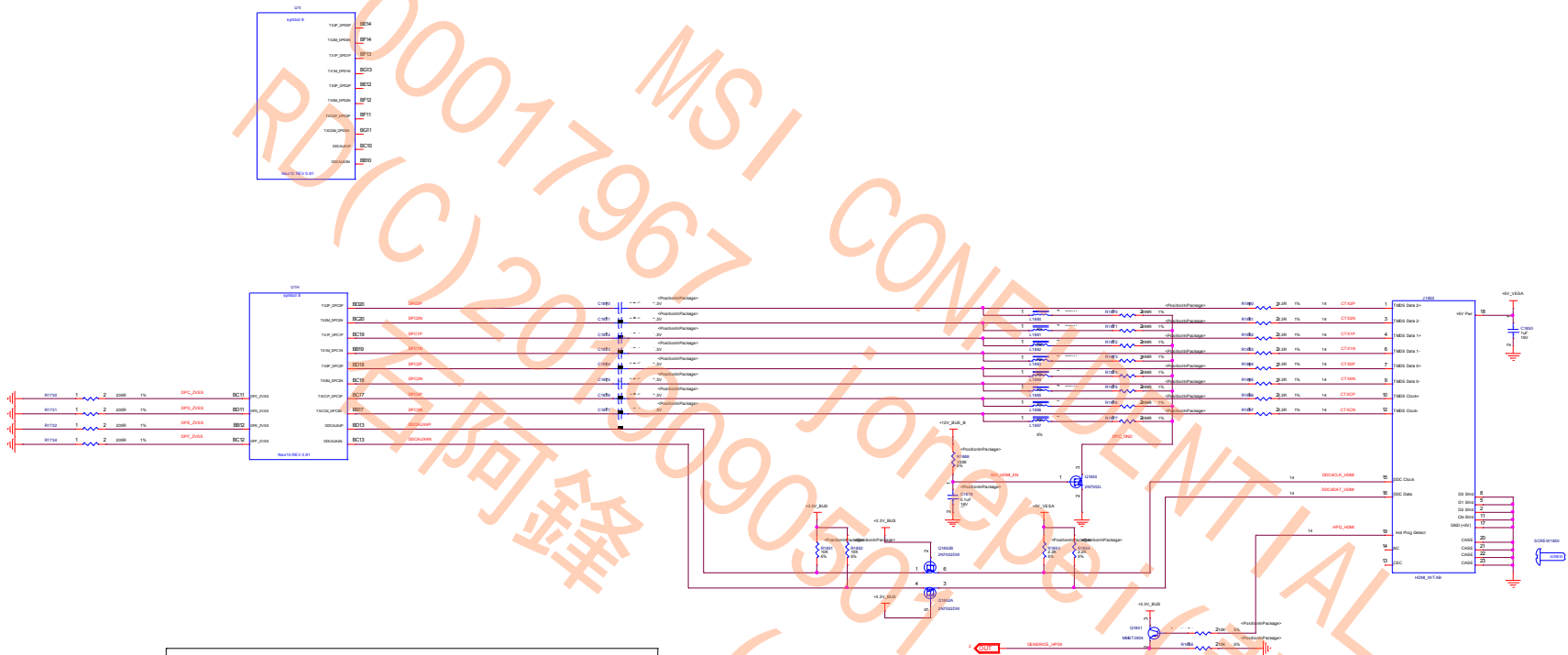
# NAVI10 TMDP A/B



AMD - PLATFORM HARDWARE ENG No.666, HUANKE ROAD SHANGHAI, CHINA 201203		
SHEET: NAVI10 TMDP-A - DP		
DATE: F4 Jun 21 03:45:48 2019	REV: 1.00	
SHEET NUMBER: 13	OF	35
DOCUMENT NUMBER: 101_0198x_088		
NOTES: NOTE		

TITLE: NAVI10 G6X16mode DP DP HDMI DP

# NAVI10 TMDP C/D

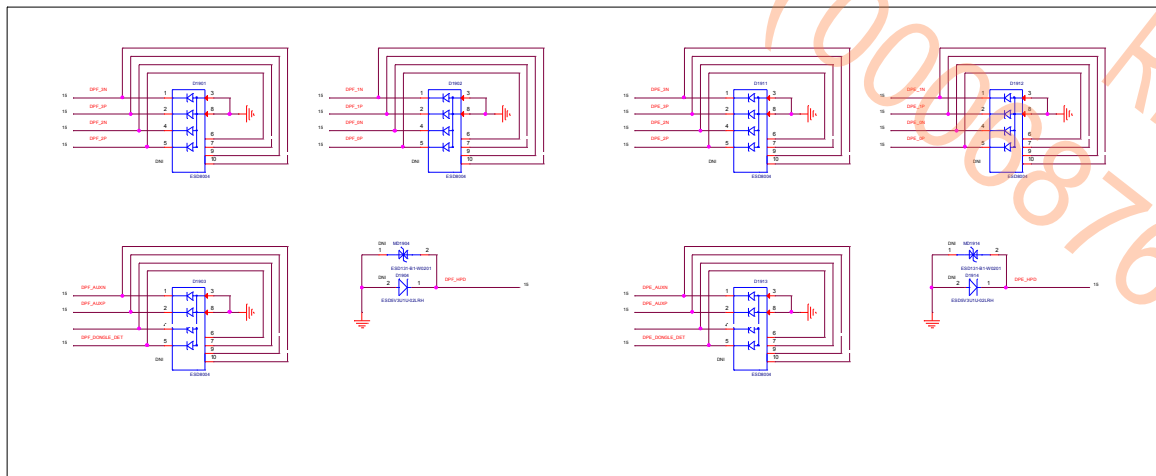
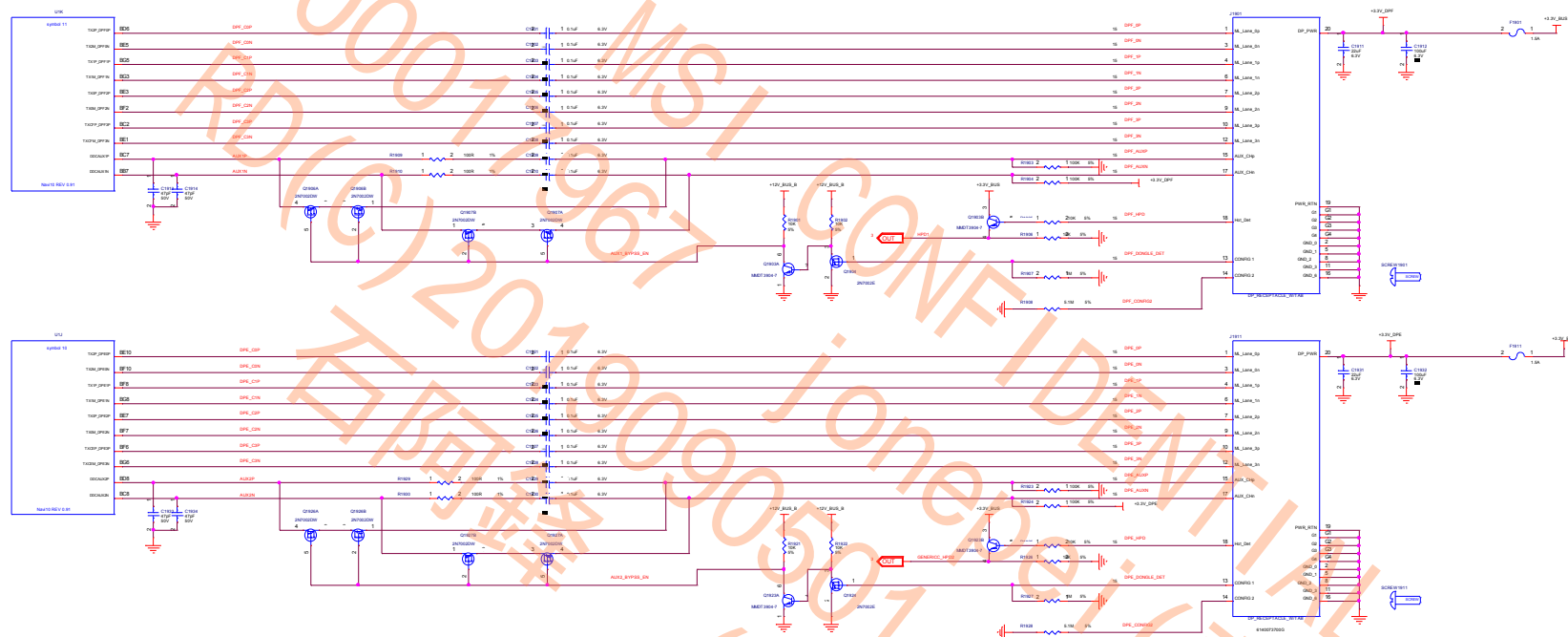


<b>AMD - PLATFORM HARDWARE ENG</b> No. 666, HUANKE ROAD SHANGHAI, CHINA 201203	
SHEET: NAVI10 TMDP-C-HDMI	
DATE: Fri Jun 21 03:45:48 2019	REV: 1.00
SHEET NUMBER: 14 OF 35	
DOCUMENT NUMBER: 105_0198x4_008	
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NAVI10 G6X16mode DP DP HDMI DP

(9) NAVI10 TMDP E/F

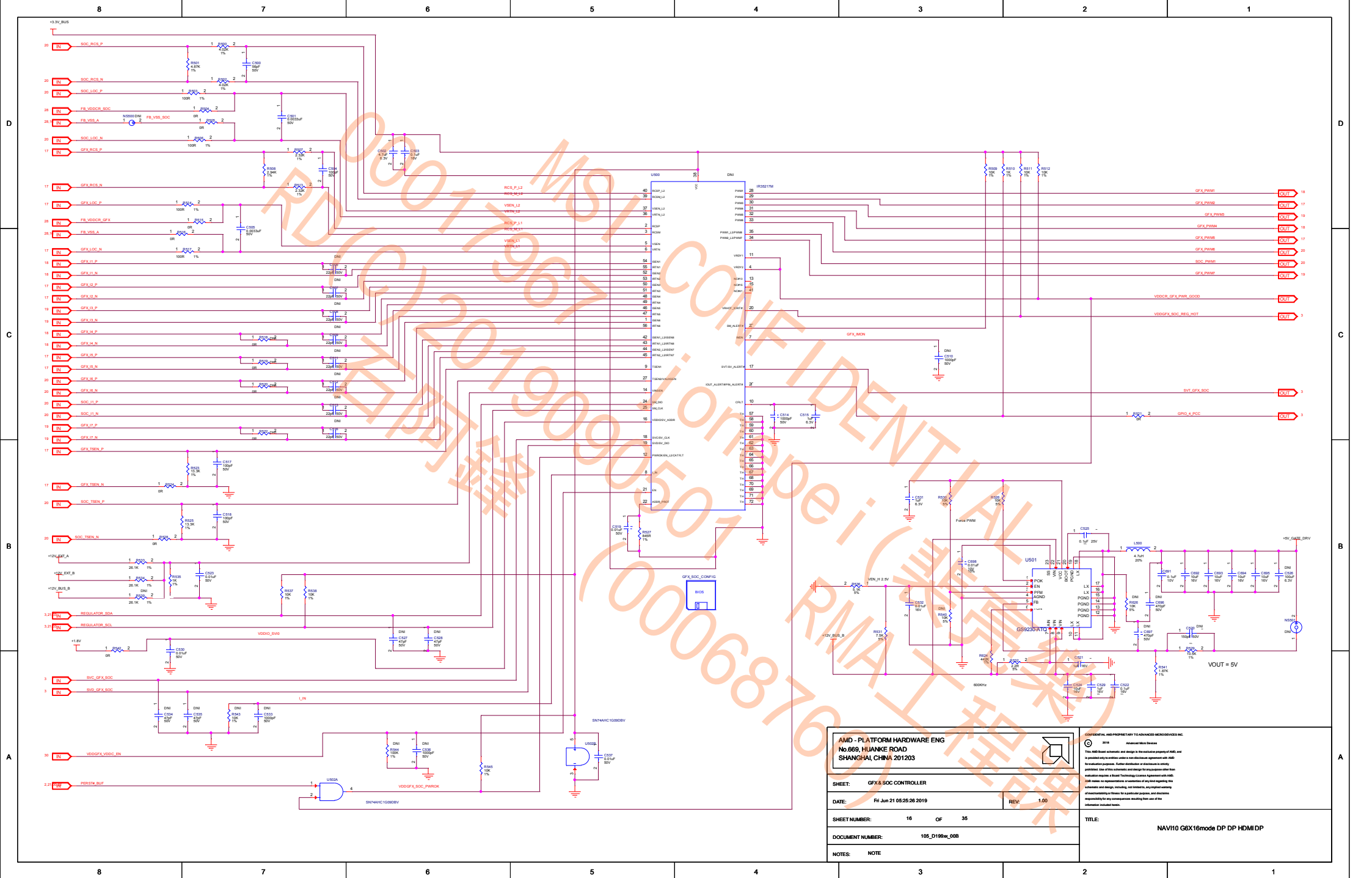


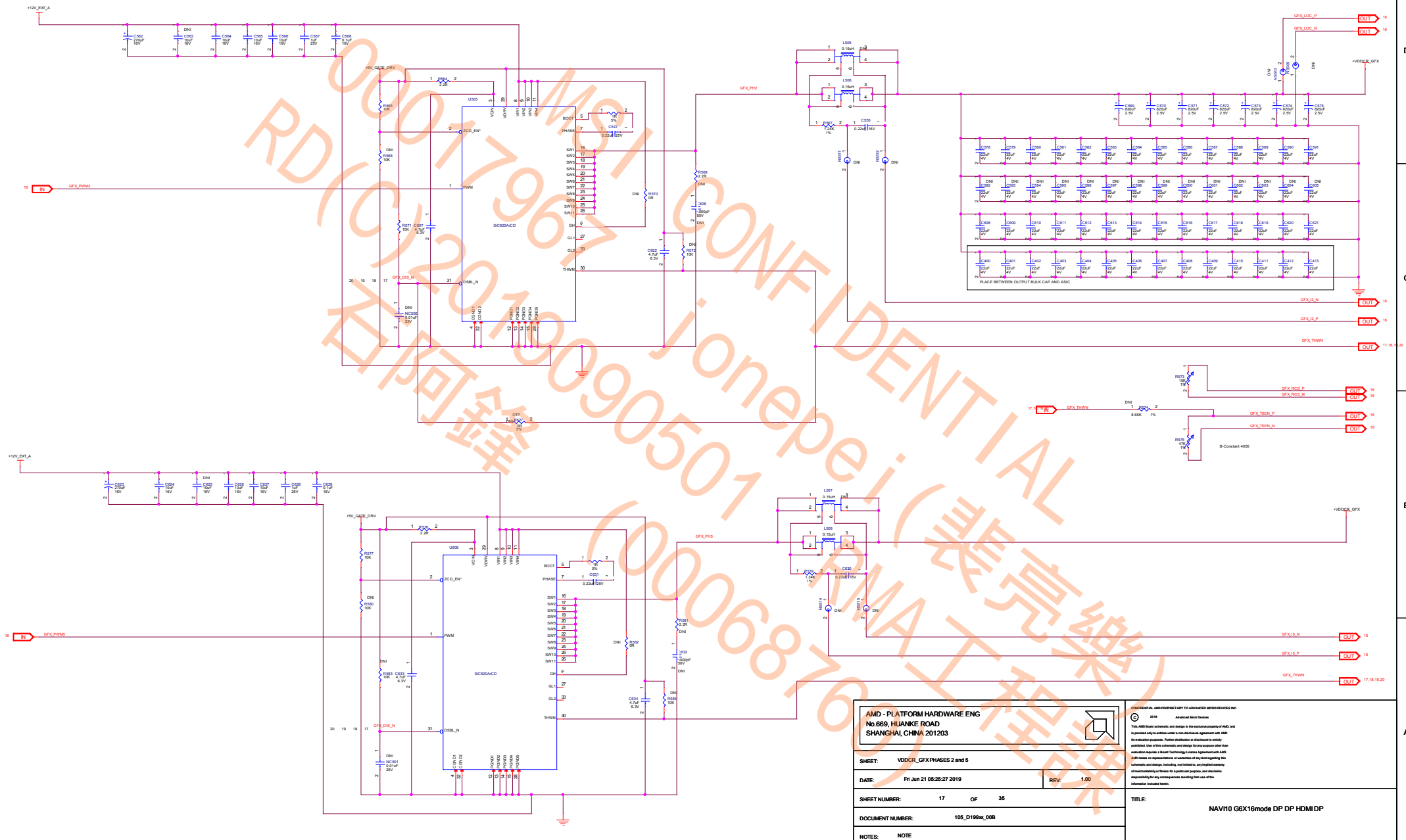
AMD - PLATFORM HARDWARE ENG	
No. 669, HUANKE ROAD	
SHANGHAI, CHINA 201203	
SHEET: NAVI10 TMDP E/F - DP DP	
DATE: Fri Jan 21 03:45:49 2010	REV: 1.00
SHEET NUMBER: 15	OF 35
DOCUMENT NUMBER: 105_0190x_008	
NOTES: NOTE	

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TITLE: NAVI10 G6X16mode DP DP HDMI DP

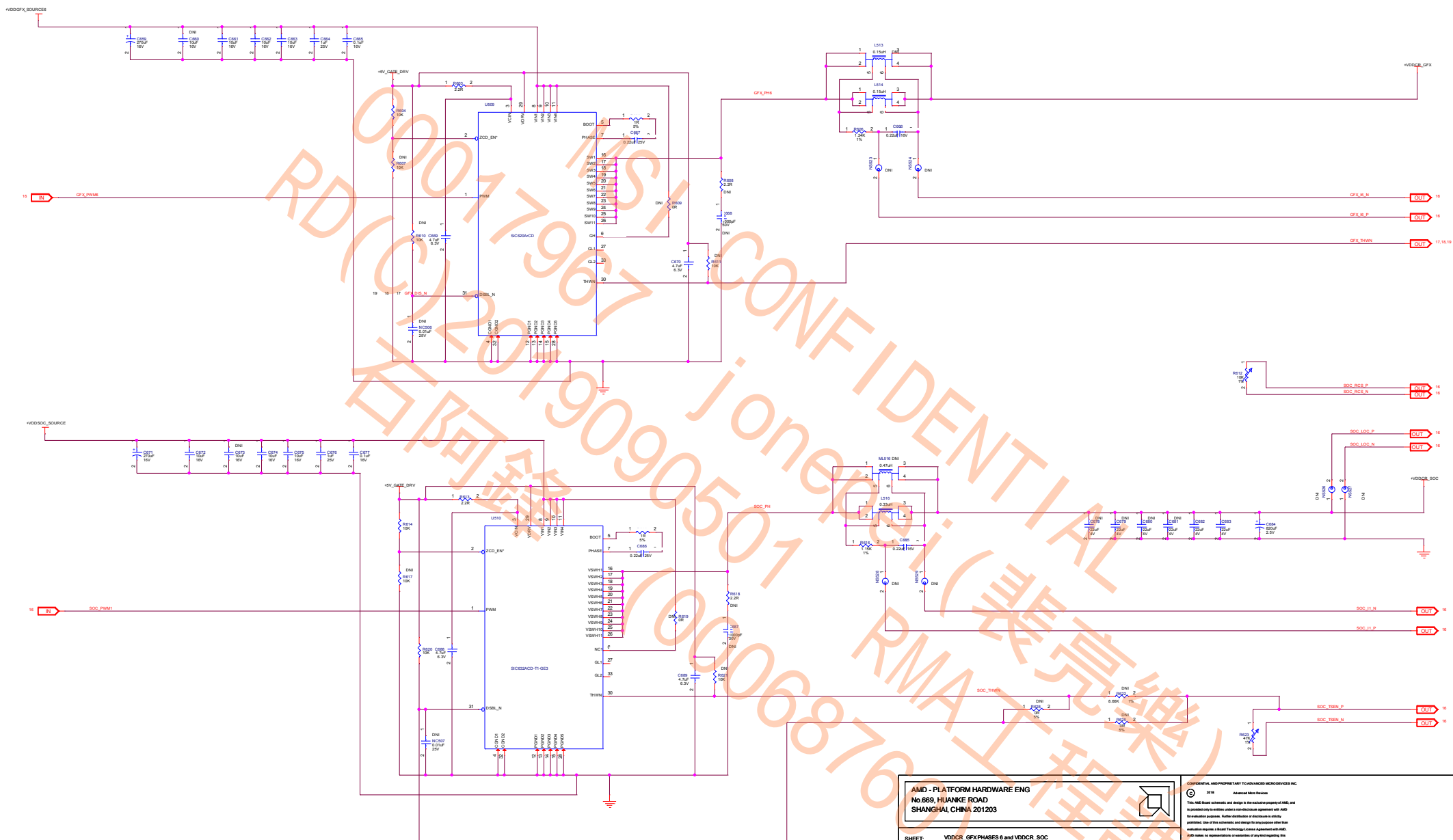


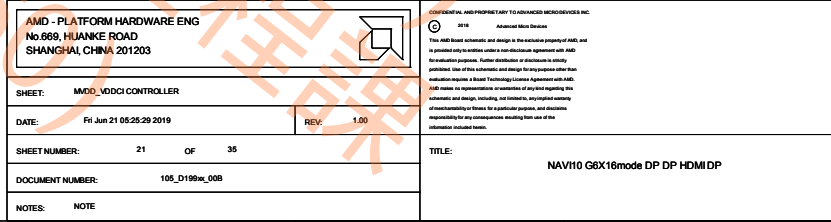














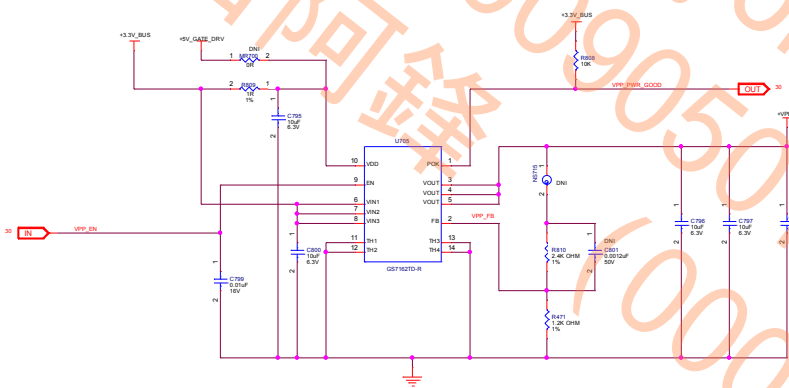
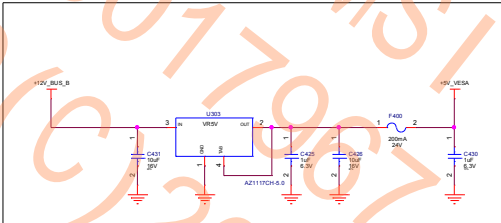








REGULATOR FOR +5V RAILS  
IOUT = 50mA



AMD - PLATFORM HARDWARE ENG  
No.669, HUANKE ROAD  
SHANGHAI, CHINA 201203

SHEET: SMALL RAIL REGULATORS

DATE: Fri Jun 21 05:25:32 2019

REV: 1.00

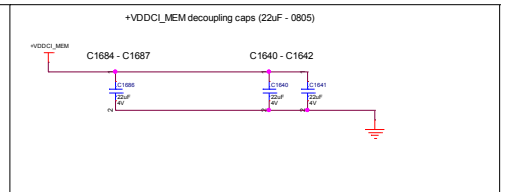
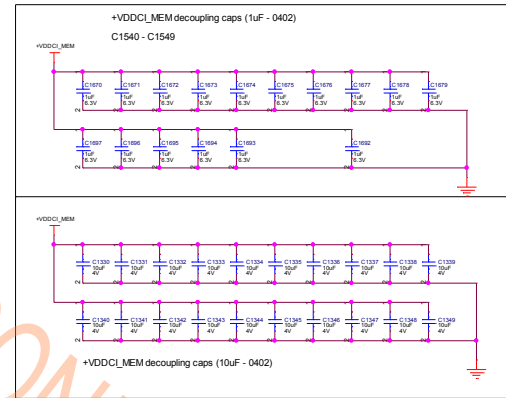
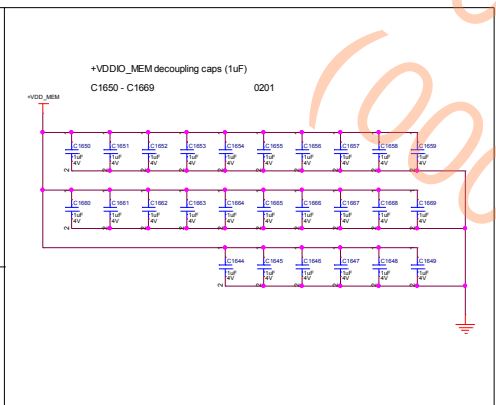
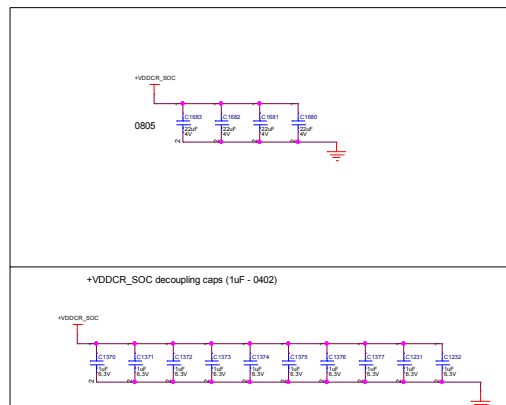
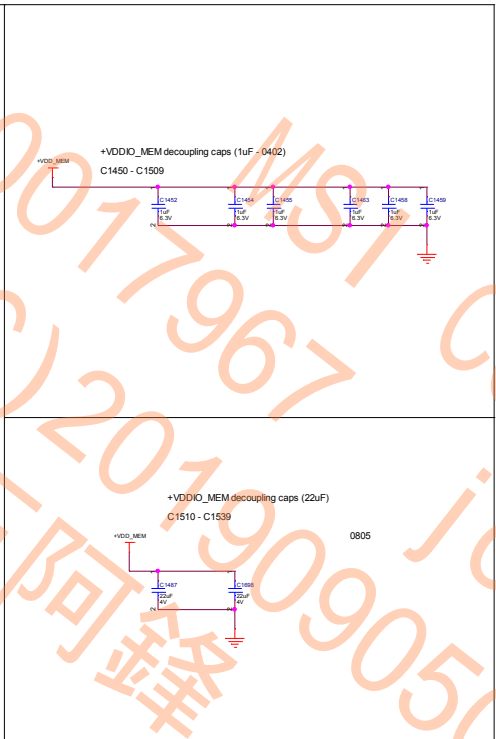
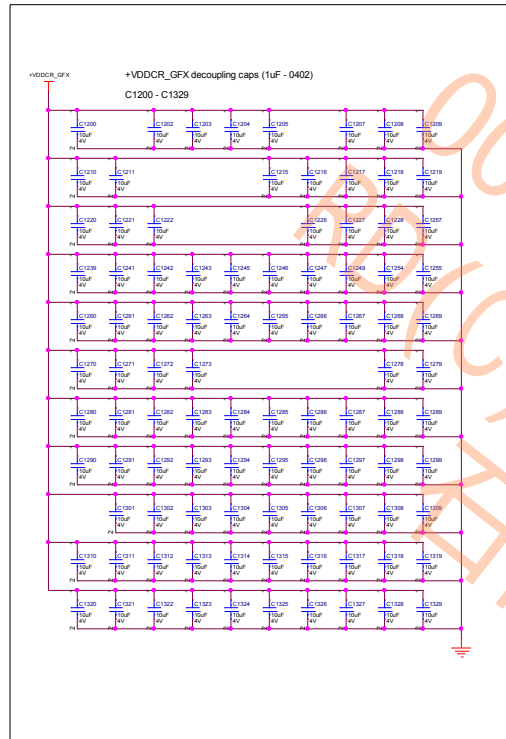
SHEET NUMBER: 26 OF 35

DOCUMENT NUMBER: 105\_D199w\_008

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TITLE: NAVI10 G8X16mode DP DP HDMI DP

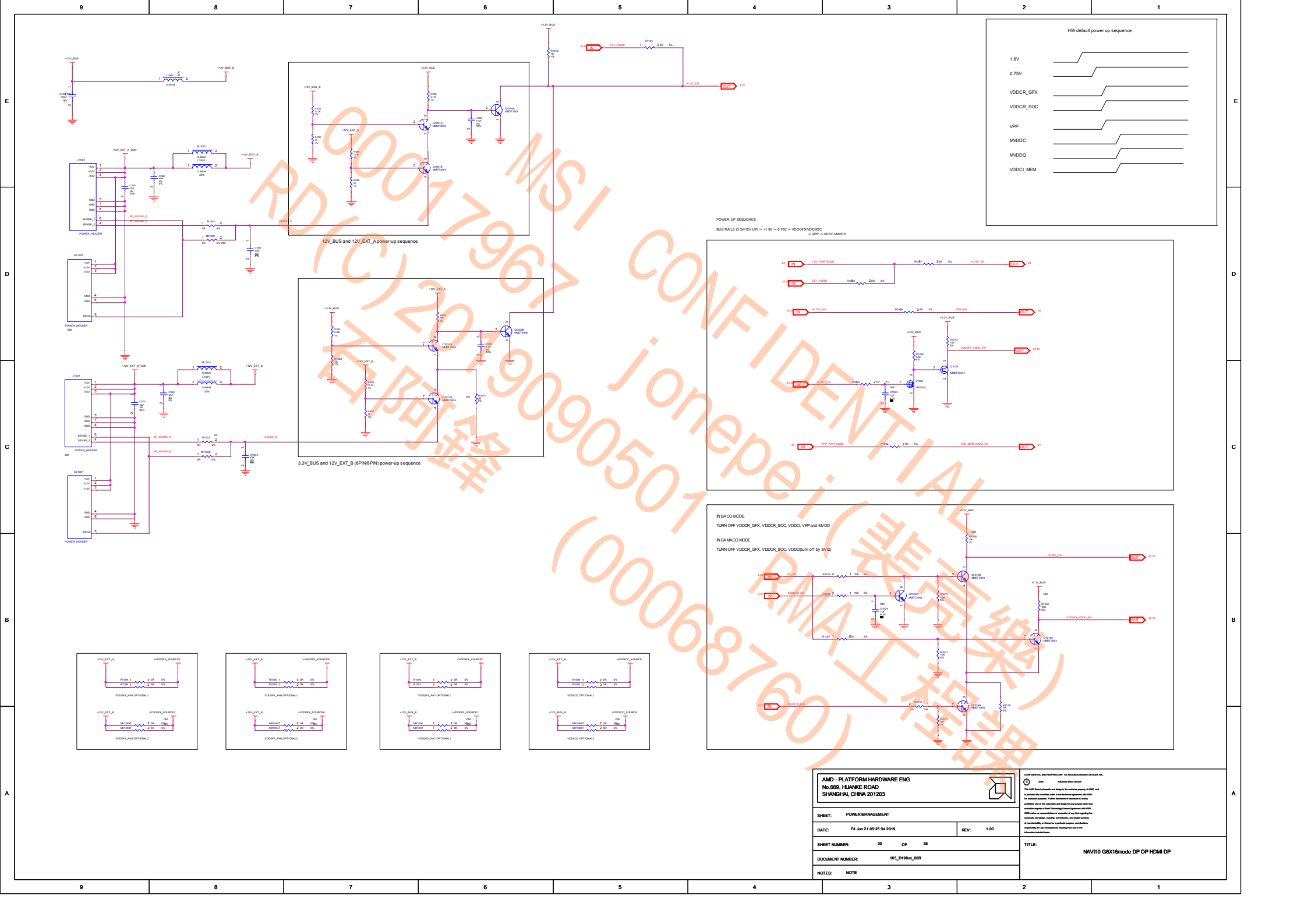


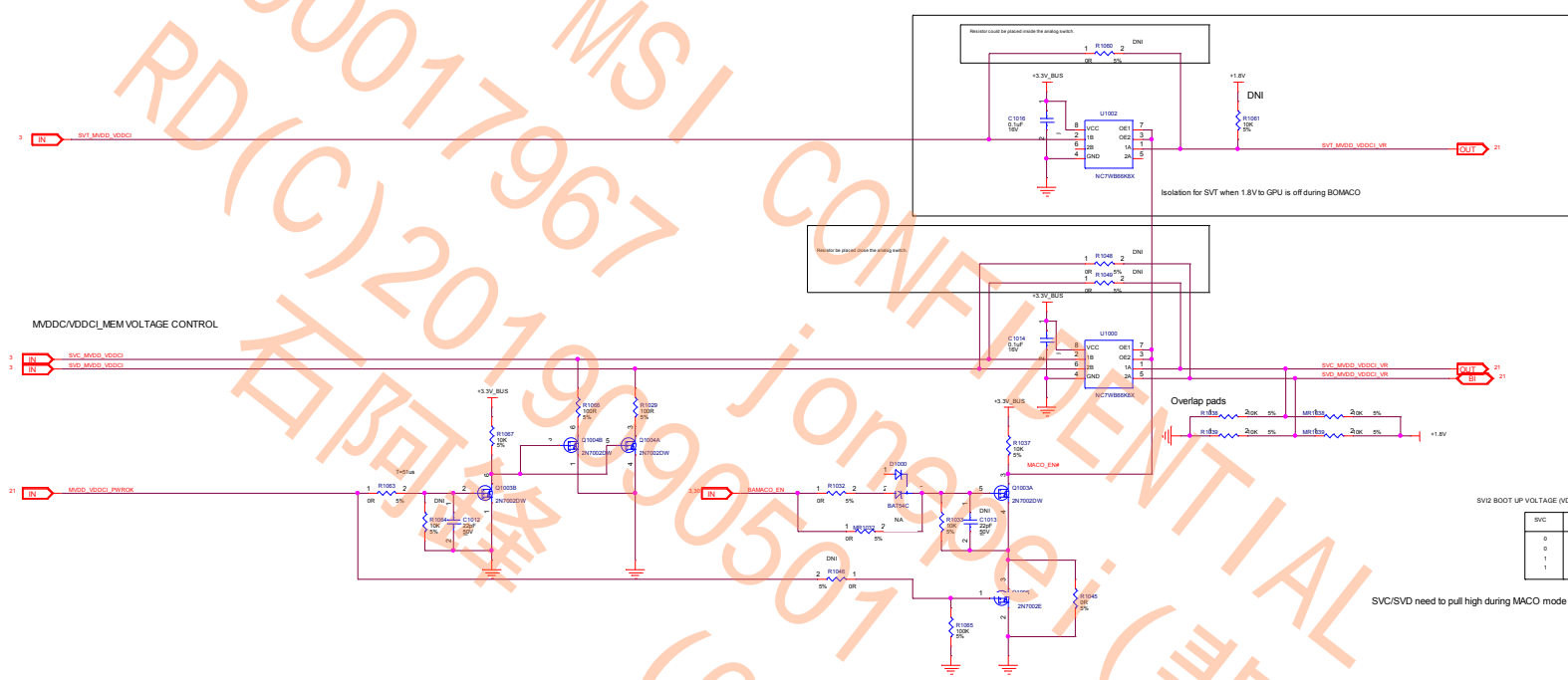
AMD - PLATFORM HARDWARE ENG No.889, HUANKE ROAD SHANGHAI, CHINA 201203	
SHEET: NAV10 DECAPS	
DATE: Fri Jun 21 05:25:32 2019	REV: 1.00
SHEET NUMBER: 27 OF 35	
DOCUMENT NUMBER: 105_D190w_008	
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TITLE: NAV10 G8X16mode DP DP HDMI DP





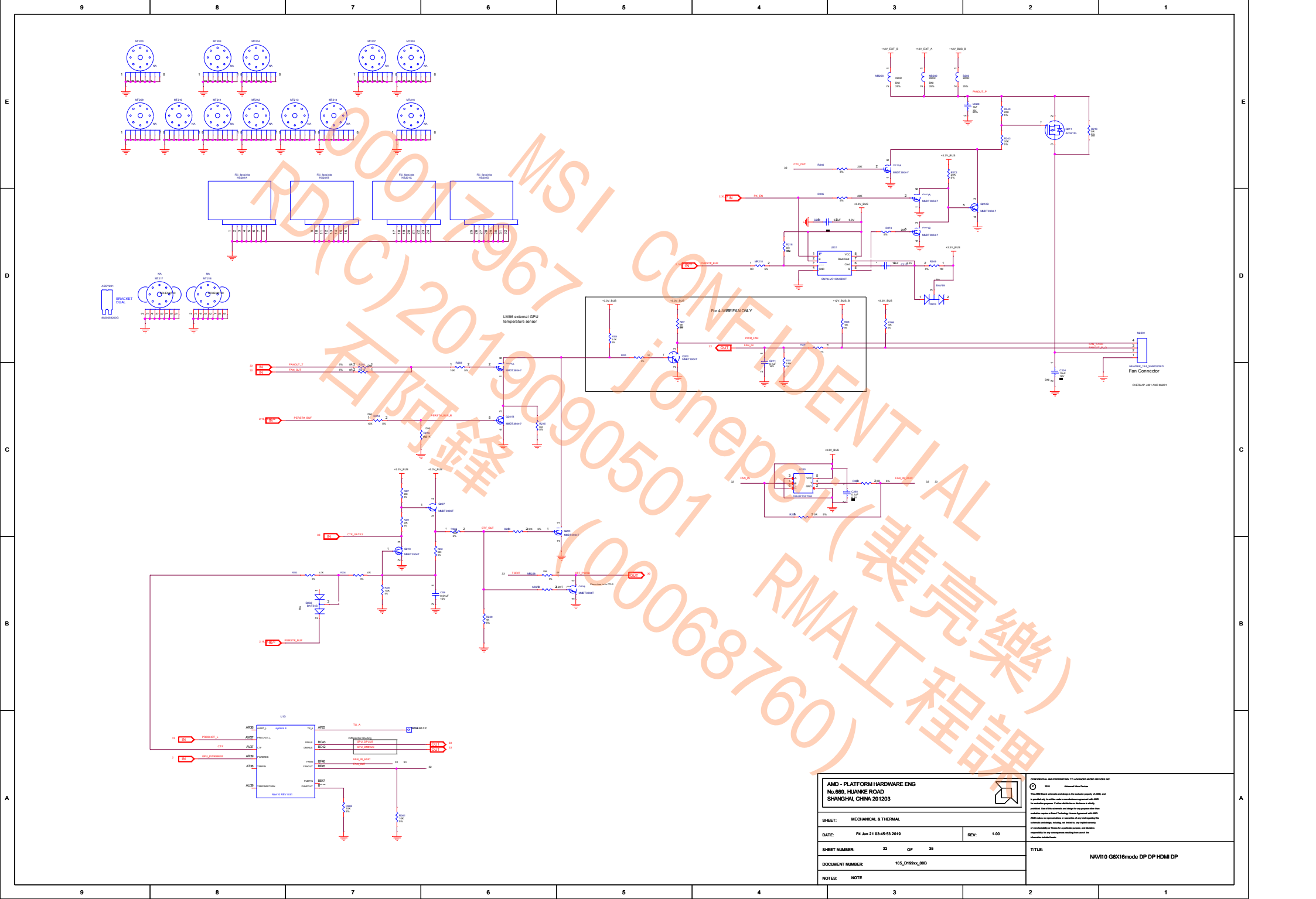


SV2 BOOT UP VOLTAGE (VDDC_LMEM/VDD)		
SV2	SVD	VOLTAJE
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V

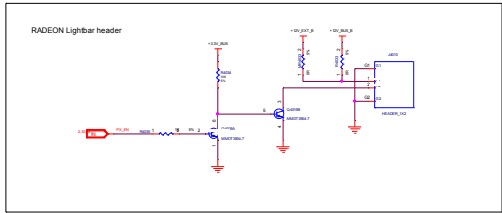
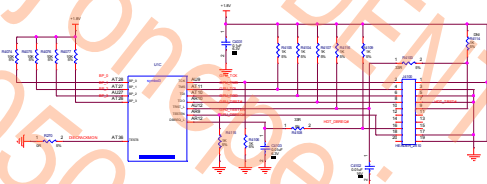
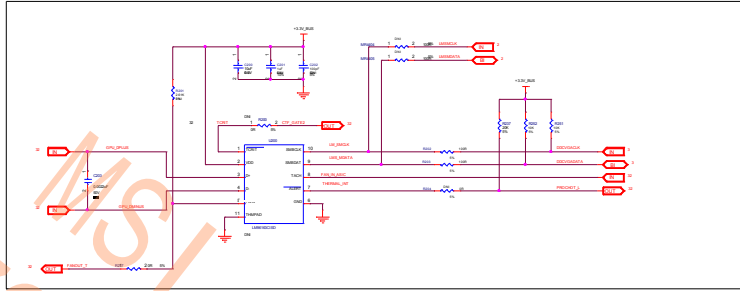
SV2/SVD need to pull high during MACO mode

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SHEET: SV2 & BAMCO	
DATE: Fri Jun 21 05:25:34 2019	REV: 1.00
SHEET NUMBER: 31	OF 35
DOCUMENT NUMBER: 105_0199w_008	
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TITLE: NAV110 G8X16mode DP DP HDMI DP







NAME: PLATFORM HARDWARE ENG NUMBER: HUANKE HEAD BRANCH: CHINA 201233			
SHEET: 000000		REV: 1.00	
DATE: Fri Jan 21 09:45:01 2011		TITLE: NAVI10 GEM10mode EP12PHM DP	
SHEET NUMBER: 33		DOCUMENT NUMBER: 10011000_000	



## REVISION HISTORY

DOCUMENT NUMBER: 105\_D199:xx\_00B

DATE: Fri Jun 21 03:44:58 2019

SHEET NUMBER: 35 OF 3

REV: 1.00

**ENGINEER:**

Peak, Dong

NOTES:

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SCH Rev	PCB Rev	Date	REVISION DESCRIPTION
1	-00A	FEB 11 2019	-00A schematics
2	-00B	JUN 21 2019	ADD PULL HIGH ON THE PWRBRKB